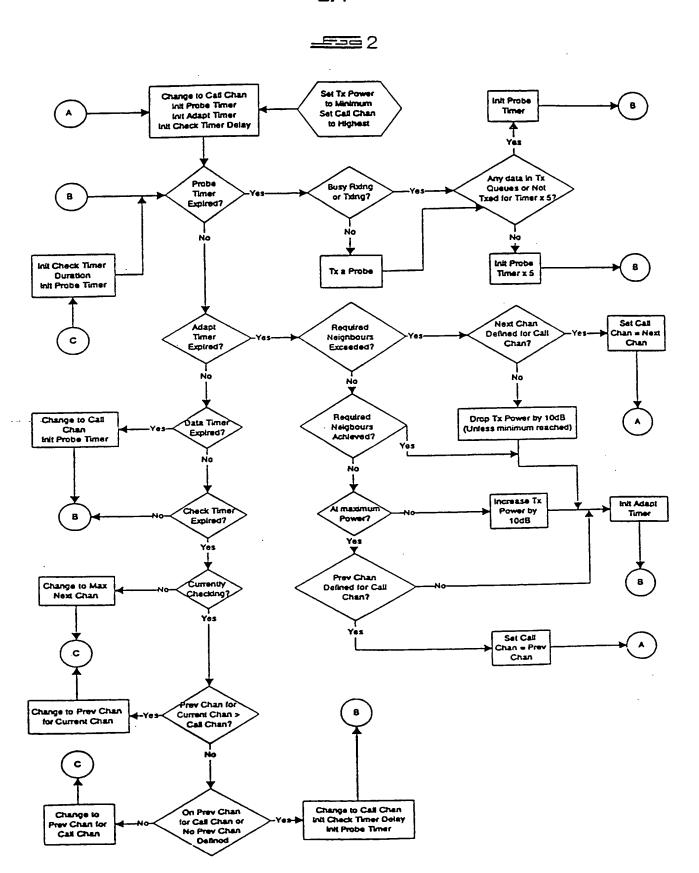


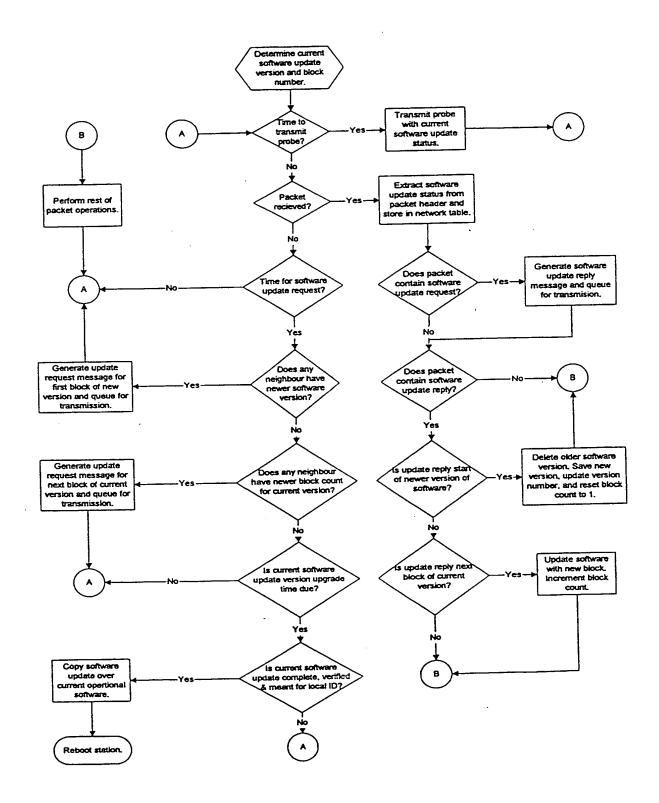
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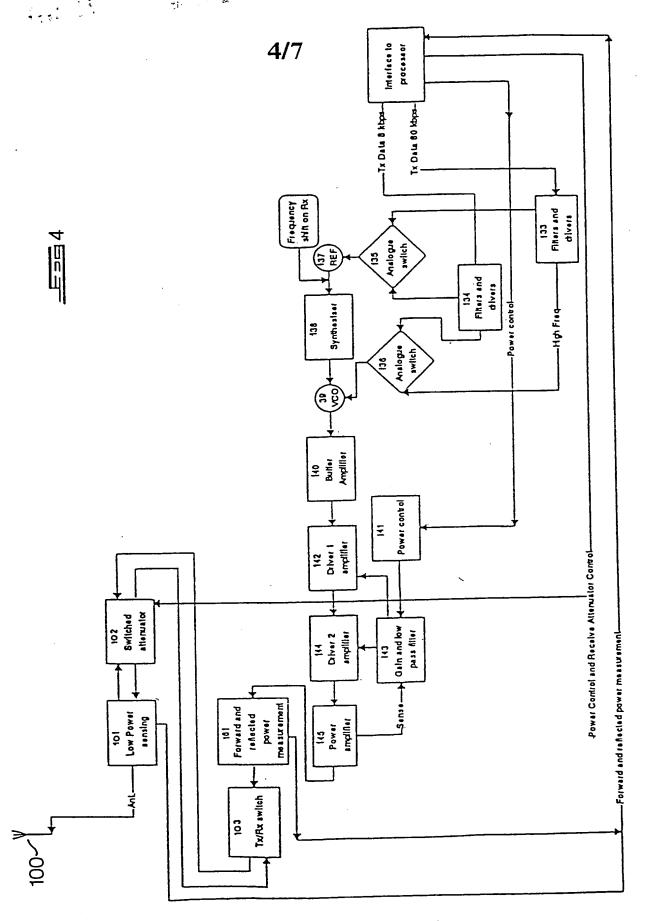


PCT/GB98/01651



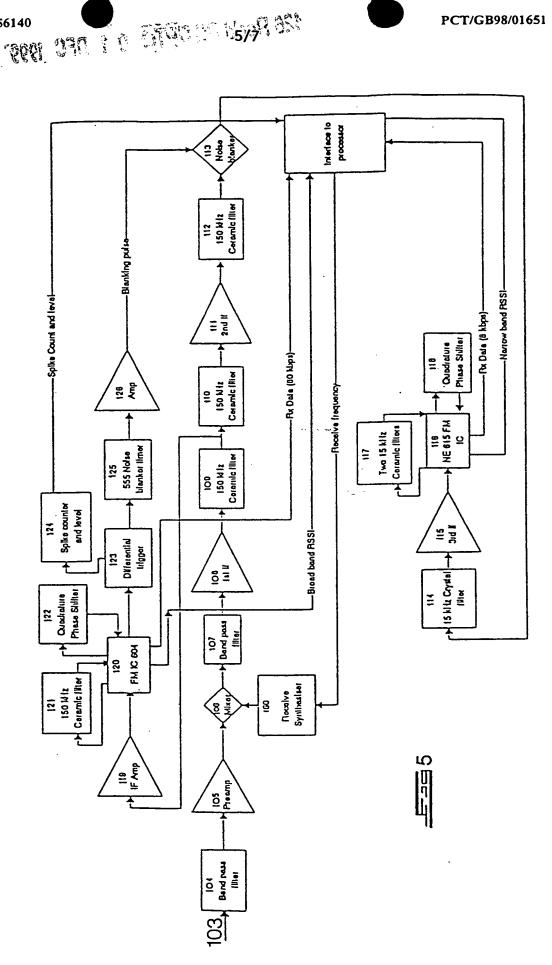


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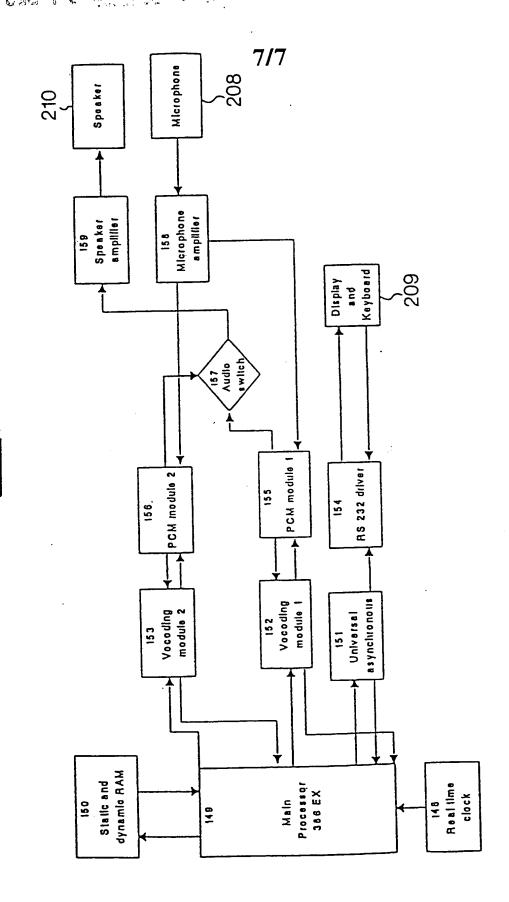


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1777 106/79 PCT/GB98/01651 WO 98/56140 STO FO Stalle and dynamic RAM Real time clock Main Processor 386 EX <del>2</del> <u>8</u> <u>\$</u> 205 dollal convertor Periphoral Interface Anabogue to -Receive frequency: chamel synchronous serial chip -Narrow band PISSI-11gh speed dal Zibg -Broad band RSSI-Ξ -Spike court and lavel-Powar cortrol--Cock —Tx Data— -Rx Dale-955 -Tx Dala--Coch -Broad band RSSI--100 -Transmit Data rate switch 207 Irtentaco to rocolver PN toquoros ercouling and encording and (booding PN sequence decoding Power control PIC 5 128 -Transmit/Receive Switch Control-133 -Pu Dala (0 Mys) -Px Data (00 Mps)--Stgral Canfity. Storel Outling-←Tx Dela--Px Dala-Y -Cbck--Ry Dalla-Tx Dole--Cbck -Power cortrol and receive attenuator control-FX 509 Modem 8 kbps OMSK FX 500 Mexbin DO KLys OWSK 2 20 -Tx Dele Trensmitter Interface 206

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